

**SANJNA SATISH REPAL**

M.E. Electronics, Mumbai University

B.E. Electrical, Mumbai University

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Cell No : 9920638618

Designation : Assistant Professor in Electronics & Telecommunication Department.

Experience : Educational 16 Years

Industry 04 Years

Date of Joining : 15/06/2005

Areas of Interest : VLSI , Embedded Systems, MicroElectronics, Finance Management

Professional Bodies Association:

- Life Member of ISTE, IETE

Interaction with Professional Institutions :

STTPs and NPTEL :

- Attended STTP course of VLSI SOC open source EDA tools, IIT Guwahati, 19 -23 Feb 2020
- Completed Integrated Circuits, MOSFETS, OP-Amp and their Applications NPTEL course with 63%
- Telecommunication Network (ISTE Chapter) at K.J. College Of Engineering, Vidyavihar 16 – 27 May 2005
- Software Tools In Electronics & Telecommunication Engineering (ISTE Chapter) at Rajiv Gandhi Institute Of Technology, Versova 15 – 19 December 2008
- Conference attended :
- NEC – 2009, National conference on Information Technology Trends in Engineering Applications. At DayanandaSagar College Of Engineering, Bangalore 19– 21 March, 2009
- Software Tools In Electronics & Telecommunication Engineering (ISTE Chapter) at Rajiv Gandhi Institute Of Technology, Versova 15 – 19 December 2008
- Recent Trends In Electronics & Instrumentation (ISTE Chapter) at Rajiv Gandhi Institute Of Technology, Versova 7 – 11 January 2008
- Advances In Wired & Wireless Communication Networks, (ISTE Chapter) at Rajiv Gandhi Institute Of Technology, Versova 11 – 15 June 2007
- NCDSP – 2009, Applications Of Digital Signal Processing, (IEEE Bombay Section) at Fr. Conceicao Rodrigues College Of Engineering 19 – 20 January 2007
- IT 606 - Embedded Systems at KRESIT, IIT Distance Education Program, Jan – May 2005