

**Full Name:**

Ms. Ankita Yadav

**Qualification:**

M.E. VLSI & Embedded System, E&TC, Savitribai Phule Pune University

B.E. Electronics & Telecommunication, Savitribai Phule Pune University

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**Designation:** Assistant Professor in Electronics & Telecommunication Department.

**Experience:** Educational 1 Year, 4 Months

**Date of Joining:** 18<sup>th</sup> July 2022

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**Areas of Interest:**

Communication & Basic Electronics, VLSI & Reconfigurable Computing.

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**Publications****Conferences:**

1. Ankita Yadav, Varsha Bendre, "Design and Verification of 16 bit RISC Processor using Vedic Mathematics", 2021 International Conference on Emerging Smart Computing and Informatics (ESCI) **[Published]**
2. Ankita Yadav, Varsha Bendre, "Design and Functional Verification of Processor using Vedic Mathematics" e-PGPEX-2020 Event (PCCOE)

**STTP and FDP :**

1. Successful Completion of One Week of Workshop on "VLSI Design at Deep Submicron Node"-2019(MIT-WPU)
2. Python Course authorized by Coursera.
3. One week Course on Design, Implementation and Verification in VLSI held by Sadeepani School of Embedded System. (27th April 2020 to 1st May 2020)
4. "VED-Quest 3 Days Workshop on Python" PCCOE(2019)

